

DECLARATION FOR UNITED STATES PATENT APPLICATION
POWER OF ATTORNEY, DESIGNATION OF CORRESPONDENCE ADDRESS

Attorney Docket
33419-190319

As a below named inventor, I hereby declare that my residence, post office address and citizenship are as stated below next to my name, and that I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled Heterojunction Organic Semiconductor Field Effect Transistor (FET) with a Gate Insulation Layer and Manufacturing Process Thereof the specification of which

[X] is attached hereto.

[] was filed on _____, as Application Serial No. _____, and was amended on _____ [if applicable].

[] was filed under the Patent Cooperation Treaty on _____ Serial No. _____ the United States of America being designated, and was amended on _____ [if applicable].

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, 1.56.

I HEREBY CLAIM foreign priority benefits under Title 35, United States Code §119(a)-(d) of §365(b) of any foreign application(s) for patent or inventor's certificate, or §365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below any foreign application for patent or inventor's certificate or of any PCT international application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application Number	Country	Foreign Filing Date	Priority Claimed
03 1 02064.X	China	January 30, 2003	YES

I HEREBY CLAIM the benefit under Title 35, United States Code §119(e) of any United States provisional application(s) listed below.

U.S. Provisional Application Number	Filing Date

I HEREBY CLAIM the benefit under Title 35, United States Code, §120 of any United States application(s), or §365(c) of any PCT International application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code §112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application.

U.S. Patent Application Number	PCT Patent Application Number	Patent Filing Date	Parent Patent Number

DECLARATION FOR UNITED STATES PATENT APPLICATION
POWER OF ATTORNEY, DESIGNATION OF CORRESPONDENCE ADDRESS

I hereby appoint the registered attorneys and agents of VENABLE associated with the following customer number to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

26694

26694

PATENT TRADEMARK OFFICE

VENABLE is located at Suite 1000, 1201 New York Avenue, N.W., Washington, D.C. 20005-3917, Telephone: (202) 962-4800, Telefax: (202) 962-8300. Address all correspondence to VENABLE, Post Office Box 34385, Washington, D.C. 20043-9998.

The undersigned hereby authorizes the registered U.S. attorneys and agents identified herein to accept and follow instructions from the undersigned's assignee, if any, and/or, if the undersigned is not a resident of the United States, the undersigned's domestic attorney, patent attorney or patent agent, as to any action to be taken in the Patent and Trademark Office regarding this application without direct communication between U.S. attorneys and the undersigned. In the event of a change in the person(s) from whom instructions may be taken, the registered U.S. attorneys and agents identified herein will be so notified by the undersigned.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Signature: Donghang Yan Date: 9 June, 2003.
First/Sole Inventor: YAN, Donghang
Citizenship: China
Residence and Post Office Address: No. 159, Renmin Street, Changchun City, Jilin Province, P. R. China 130022

Signature: Jian Zhang Date: 9 June, 2003.
Second Inventor: ZHANG, Jian
Citizenship: China
Residence and Post Office Address: No. 159, Renmin Street, Changchun City, Jilin Province, P. R. China 130022

Signature: Jun Wang Date: 9 June, 2003.
Third Inventor: WANG, Jun
Citizenship: China
Residence and Post Office Address: No. 159, Renmin Street, Changchun City, Jilin Province, P. R. China 130022

Signature: Haibo Wang Date: 9 June, 2003.
Third Inventor: WANG, Haibo
Citizenship: China
Residence and Post Office Address: No. 159, Renmin Street, Changchun City, Jilin Province, P. R. China 130022

DECLARATION FOR UNITED STATES PATENT APPLICATION
POWER OF ATTORNEY, DESIGNATION OF CORRESPONDENCE ADDRESS

Signature: Xuan Jun Yan

Date: 9. June, 2003.

Third Inventor: YAN, Xuanjun

Citizenship: China

Residence and Post Office Address: No. 159, Renmin Street, Changchun City, Jilin Province, P. R. China 130022

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: **Donghang YAN et al.**

Serial No.: **NOT ASSIGNED**

Art Unit: **NOT ASSIGNED**

Filed: **July 9, 2003**

Examiner: **NOT ASSIGNED**

For: **Heterojunction Organic Semiconductor Field Effect Transistor (FET) with a Gate Insulation Layer and Manufacturing Process Thereof**

STATEMENT OF SMALL ENTITY STATUS


Commissioner of Patents
P.O. box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 37 CFR §1.27, attached hereto is a Verified Statement Claiming Small Entity Status -- Small Business Concern which has been executed by the Assignee(s) of the entire interest.

It is respectfully requested that Applicant be granted the benefit of the Small Entity Status and that receipt of this status document be acknowledged in due course.

Respectfully submitted



Fei-Fei Chao, Ph.D.
Reg. No. 43,538

Date: July 9, 2003

Venable, Baetjer, Howard & Civiletti, LLP
1201 New York Avenue, N.W.
Suite 1000
Washington, D.C. 20005
(202) 216-8011